

NOTICE: This correspondence chart is provided for informational purposes only. It is not a part of the official Patent Application.

CORRESPONDENCE CHART

5	
	10 computer array
	12 computer
	12a memory controller computer
	14 die
10	16 data line
	18 flash memory
	20 RAM
	22 serial data line
	24 parallel data bus
15	26a through 26p input/output circuits
	28 processor
	30 ROM
	32 computer RAM
	34 computational core
20	36 computer array method
	38 provide interconnected computers
	40 configure computers for tasks
	42 assign tasks to computers
	44 initialize computers
25	

Fig. 1

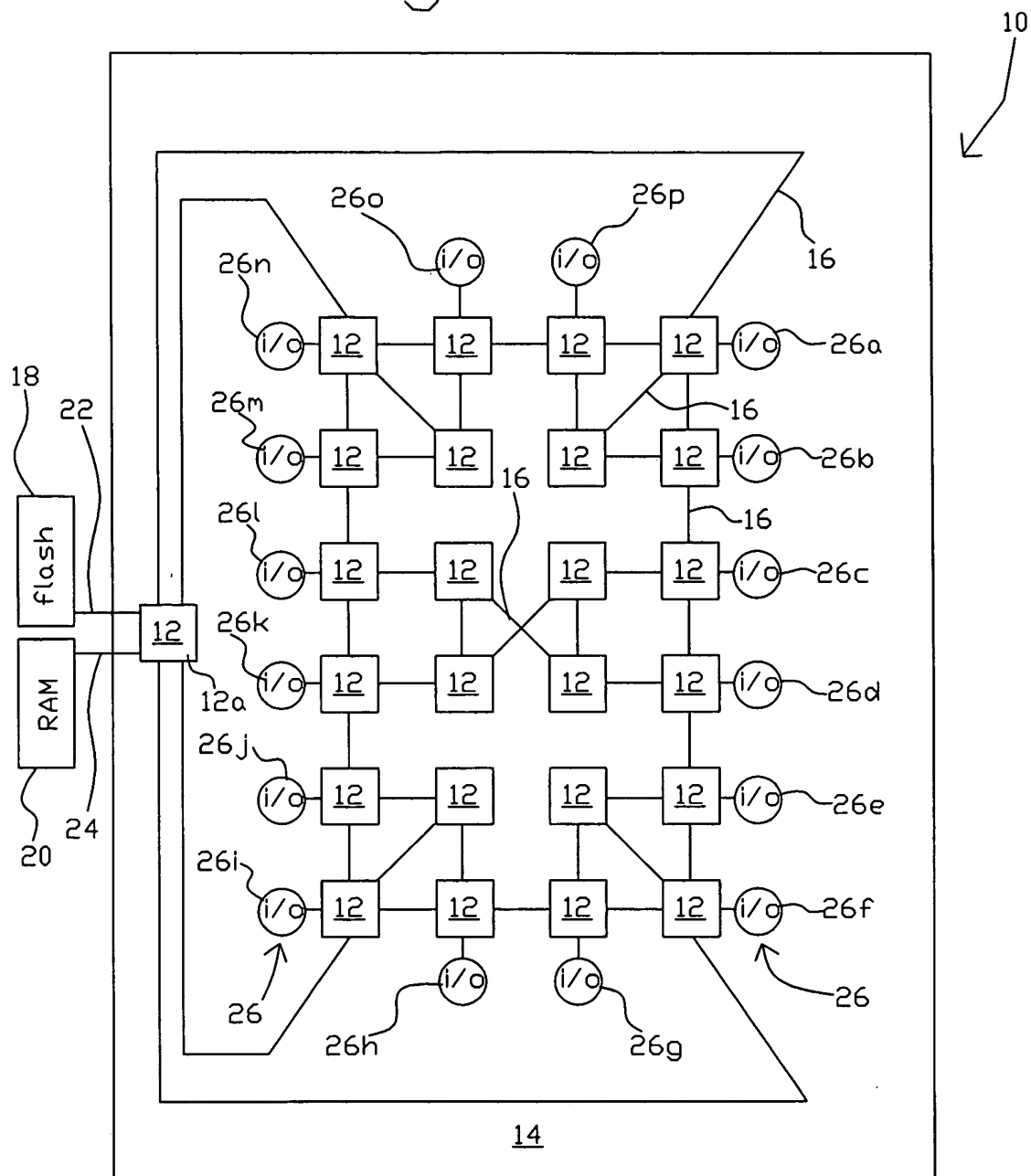


Fig. 2

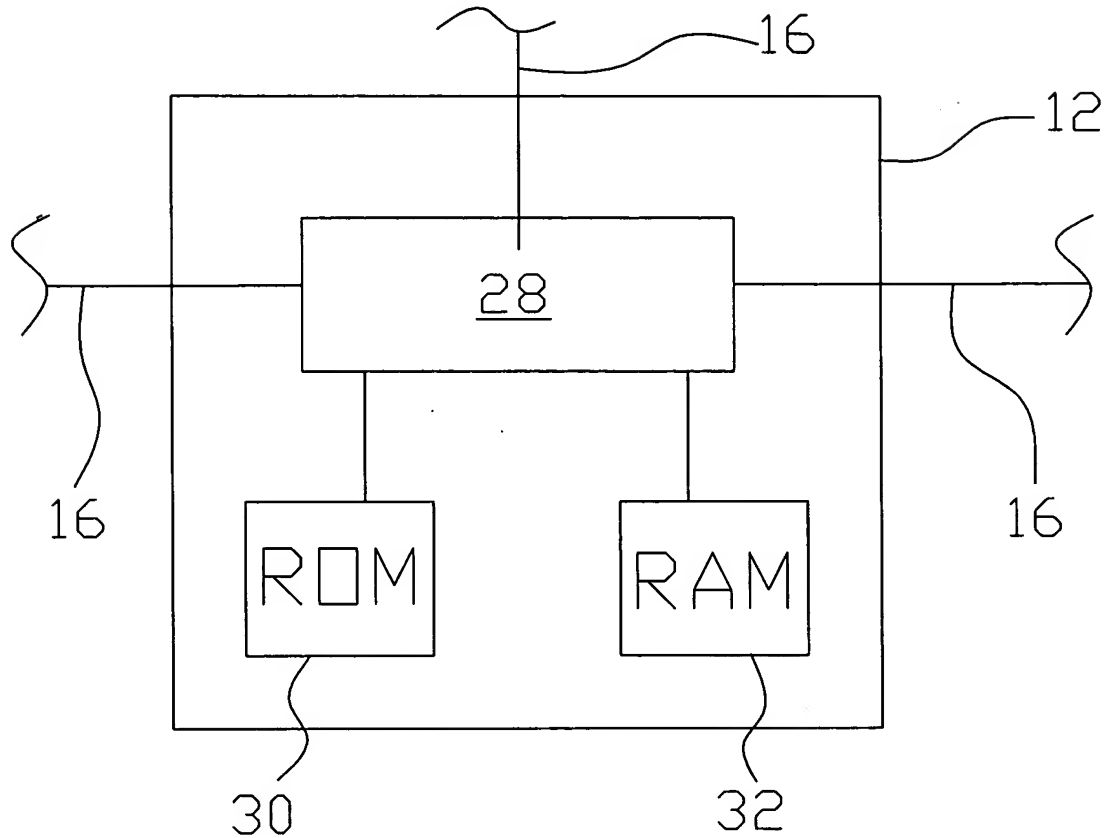


Fig. 4

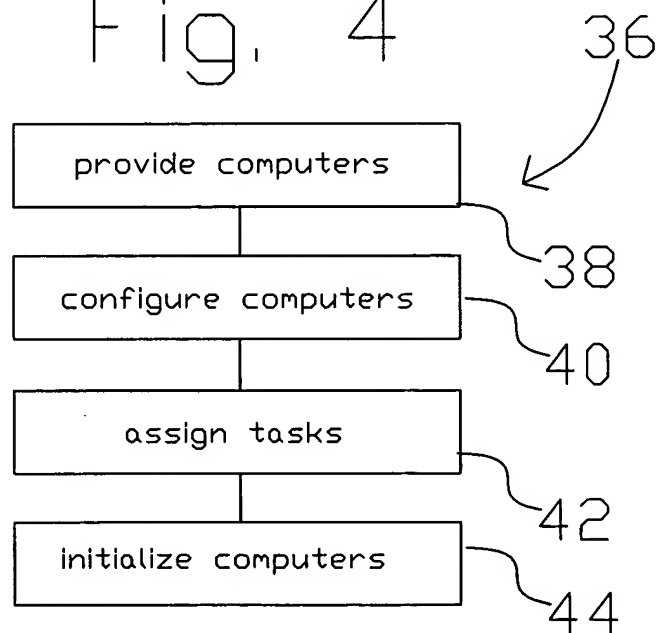


Fig. 3

